

L Number	Hits	Search Text	DB	Time stamp
1	18238	(714/5,25,29,30,710,715,718,724,725,726,727,728,729,733 365/200,201 324/765).ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/20 16:19
-	5805	(SCAN or scan near4 chain or 1149.1 or JTAG or boundary-scan or boundary adj scan) and (state adj machine or LFSR) and (memor\$3 RAM SRAM \$2RAM)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/16 10:05
-	78187	((SCAN or scan near4 chain or 1149.1 or JTAG or boundary-scan or boundary adj scan) and (state adj machine or LFSR) and (memor\$3 RAM SRAM \$2RAM)) and ASIC application\$1specific\$1integrated\$1circuit FPGA (programmable near3 (logic gate adj array)) PLA PAL	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/15 15:58
-	2710	((SCAN or scan near4 chain or 1149.1 or JTAG or boundary-scan or boundary adj scan) and (state adj machine or LFSR) and (memor\$3 RAM SRAM \$2RAM)) and (ASIC application\$1specific\$1integrated\$1circuit FPGA (programmable near3 (logic gate adj array)) PLA PAL)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/15 16:07
-	1452	((((SCAN or scan near4 chain or 1149.1 or JTAG or boundary-scan or boundary adj scan) and (state adj machine or LFSR) and (memor\$3 RAM SRAM \$2RAM)) and (ASIC application\$1specific\$1integrated\$1circuit FPGA (programmable near3 (logic gate adj array)) PLA PAL)) and (read same write)) and (telecommunication \$4communication)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/15 16:14
-	109	((((SCAN or scan near4 chain or 1149.1 or JTAG or boundary-scan or boundary adj scan) and (state adj machine or LFSR) and (memor\$3 RAM SRAM \$2RAM)) and (ASIC application\$1specific\$1integrated\$1circuit FPGA (programmable near3 (logic gate adj array)) PLA PAL)) and (read same write)) and (divid\$3 with scan)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/15 16:03
-	1820	((SCAN or scan near4 chain or 1149.1 or JTAG or boundary-scan or boundary adj scan) and (state adj machine or LFSR) and (memor\$3 RAM SRAM \$2RAM)) and (((SCAN or scan near4 chain or 1149.1 or JTAG or boundary-scan or boundary adj scan) and (state adj machine or LFSR) and (memor\$3 RAM SRAM \$2RAM)) and (ASIC application\$1specific\$1integrated\$1circuit FPGA (programmable near3 (logic gate adj array)) PLA PAL)) and (read same write))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/15 16:08
-	0	((((SCAN or scan near4 chain or 1149.1 or JTAG or boundary-scan or boundary adj scan) and (state adj machine or LFSR) and (memor\$3 RAM SRAM \$2RAM)) and (((SCAN or scan near4 chain or 1149.1 or JTAG or boundary-scan or boundary adj scan) and (state adj machine or LFSR) and (memor\$3 RAM SRAM \$2RAM)) and (ASIC application\$1specific\$1integrated\$1circuit FPGA (programmable near3 (logic gate adj array)) PLA PAL)) and (read same write))) not (((SCAN or scan near4 chain or 1149.1 or JTAG or boundary-scan or boundary adj scan) and (state adj machine or LFSR) and (memor\$3 RAM SRAM \$2RAM)) and (ASIC application\$1specific\$1integrated\$1circuit FPGA (programmable near3 (logic gate adj array)) PLA PAL)) and (read same write))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/15 16:08

-	109	((((SCAN or scan near4 chain or 1149.1 or JTAG or boundary-scan or boundary adj scan) and (state adj machine or LFSR) and (memor\$3 RAM SRAM \$2RAM)) and (((SCAN or scan near4 chain or 1149.1 or JTAG or boundary-scan or boundary adj scan) and (state adj machine or LFSR) and (memor\$3 RAM SRAM \$2RAM)) and (ASIC application\$1specific\$1integrated\$1circuit FPGA (programmable near3 (logic gate adj array)) PLA PAL)) and (read same write))) and (((((SCAN or scan near4 chain or 1149.1 or JTAG or boundary-scan or boundary adj scan) and (state adj machine or LFSR) and (memor\$3 RAM SRAM \$2RAM)) and (ASIC application\$1specific\$1integrated\$1circuit FPGA (programmable near3 (logic gate adj array)) PLA PAL)) and (read same write)) and (divid\$3 with scan))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/15 16:09
-	409	((((SCAN or scan near4 chain or 1149.1 or JTAG or boundary-scan or boundary adj scan) and (state adj machine or LFSR) and (memor\$3 RAM SRAM \$2RAM)) and (ASIC application\$1specific\$1integrated\$1circuit FPGA (programmable near3 (logic gate adj array)) PLA PAL)) and (read same write)) and (divid\$3 with scan))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/16 09:21
-	350488	(SCAN or scan near4 chain or 1149.1 or JTAG or boundary-scan or boundary adj scan)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/16 09:22
-	6517	((SCAN or scan near4 chain or 1149.1 or JTAG or boundary-scan or boundary adj scan) ) and (state adj machine or LFSR MISR)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/16 09:23
-	26	((((SCAN or scan near4 chain or 1149.1 or JTAG or boundary-scan or boundary adj scan) ) and (state adj machine or LFSR MISR) ) and (Motika-\$.in. or Koprowski-\$.in.)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/16 09:24
-	20	Six-L\$.in.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/16 10:05
-	1820	((((SCAN or scan near4 chain or 1149.1 or JTAG or boundary-scan or boundary adj scan) and (state adj machine or LFSR) and (memor\$3 RAM SRAM \$2RAM)) and (ASIC application\$1specific\$1integrated\$1circuit FPGA (programmable near3 (logic gate adj array)) PLA PAL)) and (read same write)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/16 10:40
-	1859	((((SCAN or scan near4 chain or 1149.1 or JTAG or boundary-scan or boundary adj scan) and (state adj machine or LFSR FSM MISR) and (memor\$3 RAM SRAM \$2RAM)) and (ASIC application\$1specific\$1integrated\$1circuit FPGA (programmable near3 (logic gate adj array)) PLA PAL)) and (read same write)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/16 11:57
-	1008	714/726.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/16 12:52
-	471	714/726.ccls. and scan near4 (chain circuit) and state	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/16 13:10
-	310	714/726.ccls. and scan near4 (chain circuit) and state and (memory RAM SRAM)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/16 13:22

-	0	scan near4 chain and (memory RAM SRAM) and state near4 machine	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/16 14:03
-	1085	scan near4 (chain circuit) and (memory RAM SRAM) and state near4 machine	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/16 14:05
-	583	scan near4 chain and (memory RAM SRAM) and state near4 machine	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/16 14:11
-	50	parallel near4 scan near4 chain and (memory RAM SRAM) and state near4 machine	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/16 14:12
-	50	(parallel near4 scan near4 chain) and (memory RAM SRAM) and state near4 machine	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/16 14:16
-	64	(parallel near2 (scan near2 chain)) and (memory RAM SRAM) and (state near4 machine LFSR MISR)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/16 14:17
-	1	"6516432".PN.	USPAT; US-PGPUB	2004/04/16 14:24
-	1	"6442720".PN.	USPAT; US-PGPUB	2004/04/16 14:24
-	24669	telecommunication and (test scan)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/16 15:04
-	0	(telecommunication and (test scan)) and multi-channel	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/16 15:04
-	777	(telecommunication and (test scan)) and multi-channel	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/16 15:05
-	1359974	(telecommunication and (test scan)) and multi-channel with single channel	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/16 15:05
-	13349	(telecommunication and (test scan)) and (multi-channel with single channel)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/16 15:06
-	777	((telecommunication and (test scan)) and multi-channel) and (multi-channel with single channel)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/16 15:09

-	511	((((telecommunication and (test scan)) and multi-channel) and (multi-channel with single channel)) and memory	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/16 15:10
-	7	((((telecommunication and (test scan)) and multi-channel) and (multi-channel with single channel)) and memory) and scan adj chain	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/16 15:07
-	38	((telecommunication and (test scan)) and multi-channel) and (multi-channel or mult adj channel) with (single-channel or single adj channel)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/16 15:57
-	22	((((telecommunication and (test scan)) and multi-channel) and (multi-channel or mult adj channel) with (single-channel or single adj channel)) and memory	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/16 15:51
-	24	((telecommunication and (test scan)) and multi-channel) and (single-channel or single adj channel) and scan same memory	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/16 15:55
-	885	(single-channel or single adj channel) and scan same memory	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/16 15:56
-	146	(telecommunication and (test scan)) and (single-channel or single adj channel) and scan same memory	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/16 15:56
-	24	((telecommunication and (test scan)) and (single-channel or single adj channel) and scan same memory) and (multi-channel or mult adj channel)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/16 15:59
-	2	((telecommunication and (test scan)) and (single-channel or single adj channel) and scan same memory) and (multi-channel or mult adj channel) and scan adj chain	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/16 16:00
-	0	09722880.ap.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/19 15:54
-	3	722880.ap.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/19 15:55
-	0	"09722880".ap.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/19 16:01
-	112	Rizzolo.in.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/20 16:12